

(19)



Europäisches Patentamt

European Patent Office

Office européen des brevets



(11)

EP 0 896 300 A2

(12)

EUROPEAN PATENT APPLICATION

(43) Date of publication:

10.02.1999 Bulletin 1999/06

(51) Int. Cl.⁶: G06T 7/20, H04N 5/14,
H04N 7/36

(21) Application number: 98114578.2

(22) Date of filing: 03.08.1998

(84) Designated Contracting States:

AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE

Designated Extension States:

AL LT LV MK RO SI

(30) Priority: 07.08.1997 JP 212826/97

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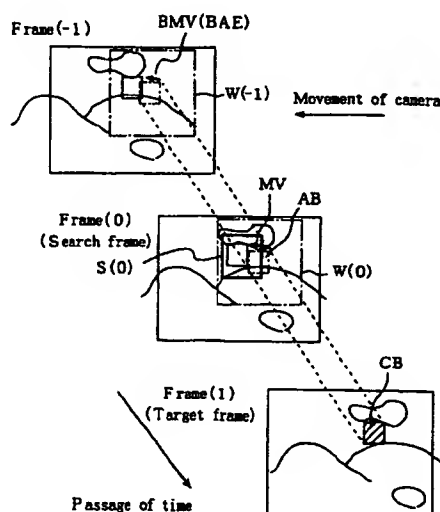
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(54) Device and method for motion vector detection

(57) According to the invention, a motion vector can be accurately and efficiently detected with regard to any type of images having a variety of characteristics. A motion vector detection unit detects a motion vector with regard to a coding target block on the basis of correlation with a candidate block within a search range. A search range storing memory stores pixel data of an original search range, and supplies, to the motion vector detection unit, pixel data of a search range in accordance with a memory address. A control unit sets the size and the position of the search range on the basis of the motion vector and the correlation evaluation value supplied from the motion vector detection unit, and generates pixel widths in the horizontal and vertical directions of the search range and an offset value corresponding to the relative position to the original search range. An address generation unit generates the memory address to be supplied to the search range storing memory on the basis of the pixel widths and the offset value of the search range generated by the control unit.

Fig. 1



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Description

BACKGROUND OF THE INVENTION

[0001] The present invention relates to motion vector detection used in motion compensative prediction coding, that is, a technique of compression coding of digital dynamic image data.

[0002] As a method of realizing image coding of dynamic images, time redundancy is reduced by using information expressing from which portion of a previous picture a given portion of a current picture is moved (i.e., a motion vector). One of methods of extracting such a motion vector is designated as a block matching method.

[0003] Figure 16 is a diagram for showing the principle of the block matching method. In the block matching method, a target frame image (target frame) to be coded is compared with a search frame image (search frame) in which a motion vector is searched for, and a block most similar to (namely, having the highest correlation with) a coding target block in the target frame (namely, a best match block) is extracted from a search range in the search frame, thereby detecting a motion vector. This block matching method is widely used in the motion compensative prediction for image compression coding.

[0004] In the block matching method, correlation between one coding target block and plural candidate blocks within the search range in the search frame are respectively calculated, and a candidate block having the highest correlation is selected as the best match block, so that a positional difference between the best match block and the coding target block can be detected as a motion vector.

[0005] As an evaluation value indicating the correlation, an accumulation value of square errors or absolute errors is generally used. A difference between a coding target block and a candidate block is smaller as the correlation therebetween is higher. Therefore, when the accumulation value of square errors or absolute errors is used as the evaluation value, the evaluation value is smaller as the correlation is higher.

[0006] A motion vector is detected for the purpose of detecting, in the search frame, a block where a coding amount can be most effectively reduced in the motion compensative prediction coding. Accordingly, the accuracy in detecting a motion vector is one of significant performances of a motion vector detecting device. The motion vector detection is, however, a process requiring a particularly large amount of calculation in an image coding algorithm. Therefore, when the motion vector detection is realized by hardware, power consumption is disadvantageously large because the process requires a long period of time and a large hardware scale. Furthermore, a real time process of the detection using software is very difficult to execute at present. Under such circumstances, it is desired to realize motion vector detection having high accuracy and high processing efficiency.

[0007] In some of conventional motion vector detecting devices, a search range is made small with regard to an image with small movement, so as to shorten a processing time for the detection of a motion vector, and a search range is enlarged with regard to an image with large movement. Thus, the processing efficiency can be improved without degrading the detection accuracy.

[0008] For example, a motion vector detecting device disclosed in Japanese Laid-Open Patent Publication No. 8-32969 comprises means for evaluating correlation with a block in the same position as a process block, means for selectively controlling the size of a search range in accordance with the correlation evaluation of the block, and means for detecting a motion vector by evaluating correlations with respective candidate blocks in the search range. In this device, the search range is reduced as the correlation of the block in the same position is increased. As a result, a motion vector can be accurately detected with a small amount of calculation.

[0009] Such a conventional motion vector detecting device has, however, the following problems:

[0010] A picture shot by a camera or the like occasionally includes so-called pan images in which the entire image is moved in a given direction. In a pan image, a shot substance itself is not changed but the position thereof is moved in the given direction.

[0011] In the aforementioned conventional motion vector detecting device, the size of the search range is set through the correlation evaluation with a block in the same position as the process block. Therefore, for example, in a pan image, the correlation in the same position is small, and hence, a large search range is inevitably set. Also, prior to the motion vector detection, it is necessary to execute the correlation evaluation in the same position, and hence, the process can be disadvantageously complicated.

SUMMARY OF THE INVENTION

[0012] The object of the invention is realizing accurate and efficient motion vector detection with regard to images having a variety of characteristics, and in particular, realizing precise and rapid motion vector detection with regard to a pan image.

[0013] Specifically, the motion vector detecting device of this invention comprises motion vector detecting means for calculating a correlation evaluation value indicating correlation between a coding target block in a target frame and a

candidate block within a search range of a search frame and for detecting a motion vector with regard to the coding target block on the basis of the correlation evaluation values; original search range storing means for storing pixel data of an original search range and for supplying pixel data read in accordance with a given memory address to the motion vector detecting means; control means for generating a first search width that is a pixel width in the horizontal direction of the search range, a second search width that is a pixel width in the vertical direction of the search range and an offset value indicating a relative position of the search range to the original search range; and address generating means for generating the memory address supplied to the original search range storing means on the basis of the first and second search widths and the offset value generated by the control means.

[0014] Preferably, in the motion vector detecting device, the motion vector detecting means supplies, to the control means, a detected motion vector and a correlation evaluation value corresponding to the motion vector, and the control means generates the first and second search widths and the offset value on the basis of the motion vector and the correlation evaluation value supplied by the motion vector detecting means.

[0015] Furthermore, the method of this invention of detecting a motion vector with regard to a coding target block comprises a first step of determining whether or not a predetermined area in a target frame includes a portion similar to the coding target block; a second step of, when it is determined that a similar portion is included in the first step, setting a first area in the search frame including the similar portion and smaller than the predetermined area as a search range for motion vector detection; and a third step of, when it is determined that a similar portion is not included in the first step, setting an area larger than the first area as the search range for motion vector detection.

[0016] Preferably, in the method of detecting a motion vector, determination of the first step is made on the basis of a correlation evaluation value corresponding to a motion vector detected with regard to a range setting reference block that is a block in the same position as or in the vicinity of the coding target block in a frame previous to a target frame, and in the second step, a position of the search range is set on the basis of the motion vector detected with regard to the range setting reference block.

[0017] Preferably, in the method of detecting a motion vector, the first step includes a step of executing a predetermined pixel calculation on each of a unit area including at least a part of the coding target block and plural comparison areas each having the same size as the unit area and provided in the vicinity of the same position as the coding target block in the search frame, and determination of the first step is made on the basis of results of the pixel calculation, and in the second step, a comparison area most similar to the unit area is selected from the plural comparison areas on the basis of the results of the pixel calculation, and a position of the search range is set on the basis of a position of the selected comparison area.

[0018] Moreover, the method of detecting a motion vector of this invention comprises a search range setting step of setting a search range for motion vector detection; and a motion vector detecting step of detecting a motion vector with regard to a coding target block on the basis of correlation with a candidate block in the search range, wherein the search range setting step includes a step of setting a size of the search range on the basis of a correlation evaluation value corresponding to a motion vector detected with regard to a range setting reference block that is a block in the same position as or in the vicinity of the coding target block in a frame previous to a target frame.

[0019] Alternatively, the method of detecting a motion vector of this invention comprises a search range setting step of setting a search range for motion vector detection; and a motion vector detecting step of detecting a motion vector with regard to a coding target block on the basis of correlation with a candidate block in the search range, wherein the search range setting step includes a step of setting a position of the search range is set on the basis of a motion vector detected with regard to a range setting reference block that is a block in the same position as or in the vicinity of the coding target block in a frame previous to a target frame.

BRIEF DESCRIPTION OF THE DRAWINGS

[0020]

Figure 1 is a diagram for illustrating a first embodiment of the invention, and shows timewise continuous three frame images corresponding to a panned picture;

Figures 2(a) and 2(b) are schematic diagrams for showing examples of a search range for motion vector detection according to the first embodiment, wherein Figure 2(a) shows an example of a large search range and Figure 2(b) shows an example of a small search range;

Figure 3 is a diagram for showing the structure of a motion vector detecting device according to an embodiment of the invention;

Figure 4 is a flowchart for showing an operation of a control unit of the first embodiment;

Figure 5 is a block diagram for showing the configuration of an address generation unit;

Figure 6 is a diagram for showing a relationship between an original search range and a search range for motion vector detection;

Figure 7 is a diagram for showing a result of mapping of two-dimensional pixel data of Figure 6 in a one-dimensional address space of a search range storing memory;

Figure 8 is a timing chart for showing an operation of the address generation unit of the first embodiment;

Figure 9 is a flowchart for showing an operation of a control unit according to a modification of the first embodiment;

Figures 10(a) through 10(c) are schematic diagrams for showing examples of a search range for motion vector detection according to the modification of the first embodiment;

Figure 11 is a diagram for showing a relationship between an original search range and a search range for the motion vector detection;

Figure 12 is a diagram for showing a result of mapping of two-dimensional pixel data of Figure 11 in a one-dimensional address space of a search range storing memory;

Figure 13 is a timing chart for showing an operation of an address generation unit according to the modification of the first embodiment;

Figure 14 is a diagram for showing an exemplified setting of comparison areas in a search frame according to a second embodiment of the invention;

Figure 15 is a flowchart for showing an operation of a control unit of the second embodiment; and

Figure 16 is a diagram for illustrating a block matching method.

DETAILED DESCRIPTION OF THE INVENTION

EMBODIMENT 1

[0021] Figure 1 is a diagram for showing timewise continuous three frame images, corresponding to a so-called panned picture shot with a camera moved in a leftward direction. When a panned picture is seen in a very short period of time, it can be said, in general, that a shot substance is moving at a given rate. Accordingly, in a first embodiment, in detecting a motion vector MV with regard to a coding target block CB in a target frame (frame (1)), a block AB of a search frame (frame (0)) in the same position as the coding target block CB is set as a range setting reference block, and on the basis of a motion vector BMV detected with regard to the range setting reference block AB and a correlation evaluation value BAE corresponding to the motion vector BMV, the size and the position of a search range are determined. In this manner, as is shown in Figure 1, a search range for the motion vector detection can be narrowed down to and set in an area where the image of the coding target block CB is estimated to be present in the search frame. Accordingly, a motion vector can be accurately and rapidly detected.

[0022] Figures 2(a) and 2(b) are schematic diagrams for showing examples of the search range for the motion vector detection according to this embodiment. In this embodiment, the search range can be in any of two types of sizes. Figures 2(a) and 2(b) show examples of the search range for the coding target block CB having a general size (i.e., 16 pixels in the horizontal direction by 16 pixels in the vertical direction) of an image coding standard such as MPEG. Figure 2(a) shows a search range of 46 pixels in the horizontal direction by 46 pixels in the vertical direction as an example of a large search range W, and Figure 2(b) shows a search range of 30 pixels in the horizontal direction by 30 pixels in the vertical direction as an example of a small search range S. The position of the small search range S is expressed by using a relative position to the large search range W. In this case, a vector starting at the upper left corner, that is, the search start point of the search range W and ending at the upper left corner, that is, the search start point of the search range S is defined as an offset (Hoff, Voff), and the position of the search range S is expressed by using offset values corresponding to the components of the offset. At this point, for example, when a picture is a still image and the search range S is set in the position of the coding target block CB, the offset values are, as is shown in Figure 2(b):

$$\text{Hoff} = 8$$

$$\text{Voff} = 8$$

Furthermore, a pixel width along the horizontal direction of the search range is designated as a search horizontal (H) pixel width corresponding to a first search width, and a pixel width along the vertical direction of the search range is designated as a search vertical (V) pixel width corresponding to a second search width.

[0023] Figure 3 is a diagram for showing the structure of a motion vector detecting device of this embodiment. In Figure 3, a reference numeral 10 denotes an address generation unit for generating a memory address MA on the basis of a search H pixel width, a search V pixel width and an offset, a reference numeral 20 denotes a search range storing memory corresponding to original search range storing means for storing image data of an original search range, a reference numeral 30 denotes a motion vector detection unit for calculating an evaluation value indicating correlation between a coding target block and a candidate block in a search range and for outputting a motion vector corresponding to the highest correlation and an evaluation value corresponding to this motion vector, a reference numeral 40 denotes

a control unit for determining a search H pixel width, a search V pixel width and an offset on the basis of the motion vector and the evaluation value output by the motion vector detection unit 30 and outputting the calculated values to the address generation unit 10, and a reference numeral 50 denotes a coding target block storing memory for storing image data of a coding target block. Here, the original search range is the large search range W of 46 pixels by 46 pixels.

[0024] Figure 4 is a flowchart for showing an operation of the control unit 40. Now, the operation of the control unit 40 will be described with reference to the flowchart of Figure 4 and Figure 1.

[0025] It is herein assumed that the search range for the motion vector detection is to be set with regard to the coding target block CB in the target frame. The control unit 40 supplies, to the address generation unit 10, a search H pixel width, a search V pixel width and an offset representing the search range and determined on the basis of the motion vector BMV with regard to the range setting reference block AB in the search frame and the correlation evaluation value BAE corresponding to the motion vector BMV.

[0026] In a step S1, the correlation evaluation value BAE corresponding to the range setting reference block AB is compared with a threshold value AE0. When the evaluation value BAE is found to be larger than the threshold value AE0 as a result of the comparison, it is determined that there is no portion similar to the coding target block CB in a predetermined area in the search frame because the correlation of the motion vector BMV is small. Therefore, without narrowing down the search range, the large search range W of 46 pixels by 46 pixels, that is the original search range, is set as the search range for the motion vector detection (step S2). Specifically, in this case, both the search H pixel width and the search V pixel width are defined as "46" and the offset is defined as (0, 0).

[0027] When the correlation evaluation value BAE corresponding to the range setting reference block AB is not larger than the threshold value AE0, it is determined that there is a portion similar to the coding target block CB in the predetermined area in the search frame because the correlation of the motion vector BMV is large. Therefore, the small search range S of 30 pixels by 30 pixels is set as the search range for the motion vector detection (step S3). Then, in a step S4, the size of the motion vector BMV, i.e., $|BMV(x, y)|$, of the range setting reference block AB is compared with a threshold value MV0.

[0028] When the size of the motion vector BMV is found to be larger than the threshold value MV0 as a result of the comparison, it is determined that the movement of the coding target block CB is large, and the offset is set by using the components of the motion vector BMV (step S5). Specifically, the components of the offset (Hoff, Voff) are set as follows:

$$Hoff = x + 8$$

$$Voff = y + 8$$

In other words, in this case, both the search H pixel width and the search V pixel width are defined as "30" and the offset is defined as (x+8, y+8).

[0029] When the size of the motion vector BMV is not larger than the threshold value MV0, it is determined that the movement of the coding target block CB is small and that the image therein is still, and the offset is set at (8, 8). Specifically, in this case, both the search H pixel width and the search V pixel width are defined as "30" and the offset is defined as (8, 8).

[0030] The address generation unit 10 generates a memory address MA on the basis of the search H pixel width, the search V pixel width and the offset output by the control unit 40. The motion vector detection unit 30 detects a motion vector with regard to the coding target block CB by using pixel data of the search range read from the search range storing memory 20 (step S10).

[0031] Figure 5 is a block diagram for showing the configuration of the address generation unit 10. As is shown in Figure 5, the address generation unit 10 includes an H counter 11 as a first counter for counting a clock, an H comparator 12 as a comparator for comparing the search H pixel width and the count value of the H counter 11, resetting the count value of the H counter 11 when the compared values are the same, and outputting a count-up signal CU, a V counter 13 as a second counter for counting the count-up signal CU, a V comparator 14 for comparing the search V pixel width and the count value of the V counter 13 and generating an actuation signal for completing the operation of the motion vector detection unit 30 when the compared values are the same, and a memory address converter 15 for generating the memory address MA of the search range storing memory 20 on the basis of the offset and the count values of the H counter 11 and the V counter 13. The H counter 11 and the V counter 13 are reset in accordance with a start signal supplied by the control unit 40, so that the address generation unit 10 can be actuated.

[0032] Now, the operation of the address generation unit 10 of Figure 5 will be described with reference to Figures 6 through 8.

[0033] Figure 6 is a diagram for showing a relationship between an original search range stored in the search range storing memory 20 and a search range for the motion vector detection. As is shown in Figure 6, it is herein assumed that the search range storing memory 20 stores pixel data of 12 pixels in the horizontal direction by 8 pixels in the ver-

tical direction as the original search range, and that a search range determined by the search H pixel width of 8, the search V pixel width of 6 and the offset of (2, 1) is set as the search range for the motion vector detection.

[0034] Figure 7 is a diagram resulting from mapping of the two-dimensional pixel data of Figure 6 in a one-dimensional address space of the search range storing memory 20. The address generation unit 10 generates the memory address MA to be used for reading the pixel data of the search range for the motion vector detection from the search range storing memory 20. Figure 8 is a timing chart for showing the operation of the address generation unit 10. As is shown in Figure 8, when the start signal is input, the H counter 11 starts counting the clock. When the count value of the H counter 11 becomes equal to a value indicating the search H pixel width, the H counter 12 outputs the count-up signal CU, thereby resetting the count value of the H counter 11. The V counter 13 counts the count-up signal CU. When the count value of the V counter 13 becomes equal to a value indicating the search V pixel width, the V comparator 14 outputs the actuation signal.

[0035] In response to the offset (Hoff, Voff), the memory address convertor 15 calculates an address SA0 as a starting point of the search range for the motion vector detection on the basis of a pixel width Hmax in the horizontal direction and a pixel width Vmax in the vertical direction of the original search range stored in the search range storing memory 20. The starting address SA0 is obtained by the following formula:

$$SA0 = Hmax \times Voff + Hoff$$

[0036] In the case exemplified in Figure 6, the starting address SA0 is:

$$SA0 = 12 \times 1 + 2 = 14$$

[0037] Then, the memory address convertor 15 calculates the memory address MA by the following formula:

$$MA = SA0 + Hmax \times (\text{the count value of the V counter 13}) \\ + \text{the count value of the H counter 11}$$

As a result, the memory address MA is varied as is shown in Figure 8. For example, from the starting address SA0 up to an address corresponding to the search H pixel width, the memory address MA can be obtained by adding the count value of the H counter 11 to the starting address SA0 as is shown in Figure 7. In response to the input of a subsequent clock, the count value of the H counter 11 is set at 0 and the count value of the V counter 13 is set at 1. At this point, the memory address MA is:

$$MA = 14 + 12 \times 1 + 0 = 26$$

[0038] In this manner, according to this embodiment, a motion vector can be accurately and efficiently detected even in a panned picture.

[0039] In this embodiment, a block of the search frame in the same position as the coding target block is used as the range setting reference block. Instead, a block of the search frame in the vicinity of the same position as the coding target block can be used as the range setting reference block. Also, a block in the same position or in the vicinity of the same position as the coding target block in a frame further preceding the search frame can be used as the range setting reference block.

[0040] Furthermore, although the search range of this embodiment has two types of sizes, the search range can be in a larger number of types of sizes. Also, the search H pixel width and the search V pixel width can be made variable, with the components of a motion vector with regard to a range setting reference block used as an evaluation index.

MODIFICATION OF EMBODIMENT 1

[0041] In the first embodiment, subsampling can be also adopted. In this case, the control unit 40 inputs to the address generation unit 10, in addition to the search H pixel width, the search V pixel width and the offset value, a subsample number DH in the horizontal direction of the search range as a first subsample number and a subsample number DV in the vertical direction as a second subsample number.

[0042] Figure 9 is a flowchart for showing the operation of the control unit 40 according to this modification. This flowchart is different from that shown in Figure 4 in specifying whether or not subsampling is executed in addition to setting the size and the position of the search range. Specifically, in a step S2A for setting the large search range W and in a step S5A for setting the small search range S in a position other than the position of the coding target block CB, the

execution of subsampling is specified. In a step S6A for setting the small search range S in the same position as the coding target block CB, the executing of subsampling is not specified.

[0043] Figures 10(a) through 10(c) are schematic diagrams for showing examples of the search range for the motion vector detection according to this modification. In this case, the subsample number DH in the horizontal direction and the subsample number DV in the vertical direction are both set at 2. Figure 10(a) shows an example of the large search range W adopted when the executing of subsampling is specified, Figure 10(b) shows an example of the small search range S adopted when the execution of subsampling is specified, and Figure 10(c) shows an example of the small search range S adopted when the execution of subsampling is not specified. In the step S6A, it is because the movement of the coding target block CB is determined to be small that the small search range S is set in the same position as the coding target block CB. In this case, since degradation of the detection accuracy is undesirable, the execution of subsampling is not specified as is shown in Figure 10(c).

[0044] Now, the operation of the address generation unit 10 of this modification will be described with reference to Figures 11 through 13.

[0045] Figure 11 is a diagram for showing the relationship between an original search range stored in the search range storing memory 20 and a search range for the motion vector detection. It is herein assumed, as is shown in Figure 11, that the search range for the motion vector detection is determined by defining the search H pixel width as 6, the search V pixel width as 4, the offset as (0, 0), and both the subsample numbers DH and DV as 2.

[0046] Figure 12 is a diagram resulting from mapping the two-dimensional pixel data of Figure 11 in a one-dimensional address space of the search range storing memory 20.

[0047] Figure 13 is a timing chart for showing the operation of the address generation unit 10. In this modification, the memory address converter 15 calculates a memory address MA by the following formula:

$$MA = SA0 + Hmax \times (\text{the count value of the V counter 13}) \times$$

DV

As a result, the memory address MA is varied as is shown in Figure 12.

EMBODIMENT 2

[0048] In a second embodiment of the invention, plural blocks having the same size as a coding target block are set in the search frame in the vicinity of the same position as the coding target block, and a predetermined pixel calculation is conducted with regard to the coding target block and each of the plural blocks set in the search frame. On the basis of the results of the calculation, the size and the position of the search range for the motion vector detection are set. Herein, an accumulating calculation of pixel data in the block is performed as the predetermined pixel calculation.

[0049] Figure 14 is a diagram for showing an example of the blocks set in the search frame according to this embodiment. In Figure 14, with regard to a coding target block CB of 16 pixels by 16 pixels as a unit area, nine blocks SB0 through SB8 each of 16 pixels by 16 pixels are set in the search frame as comparison areas.

[0050] A motion vector detecting device of this embodiment has the structure as is shown in Figure 3 similarly to that of the first embodiment. However, the control unit 40 does not receive a motion vector and an evaluation value output by the motion vector detection unit 30 but receives image data of each frame instead.

[0051] Figure 15 is a flowchart for showing the operation of the control unit 40 of the motion vector detecting device of this embodiment. First, in a step S21, with regard to the coding target block and each block set in the search frame, the accumulating calculation of the pixel data of the block is executed. In a step S22, a minimum absolute value S_{Amin} of a difference between an accumulation result AC(CB) of the coding target block and an accumulation result AC(SB_n) (wherein n = 0 through 8) of each block set in the search frame is compared with a threshold value SA0. When the minimum value S_{Amin} is found to be larger than the threshold value SA0 as a result of the comparison, it is determined that there is no portion similar to the coding target block CB in a predetermined area in the search frame. Therefore, without narrowing down the search range, the large search range W of 46 pixels by 46 pixels is set as the search range for the motion vector detection (step S23). Specifically, in this case, both the search H pixel width and the search V pixel width are defined as "46" and the offset is defined as (0, 0).

[0052] When the minimum value S_{Amin} is not larger than the threshold value SA0, it is determined that there is a portion similar to the coding target block CB in the predetermined area in the search frame. Therefore, the small search range S of 30 pixels by 30 pixels is set as the search range for the motion vector detection (step S24). Then, in a step S25, the offset is set so that the search range S can be set in the position of a block SB_k when the absolute value of a difference between the accumulation result of the block SB_k and the accumulation result of the coding target block CB is the minimum.

[0053] For example, in the case where the block SB0 is set in the same position as the coding target block CB and the blocks SB1 through SB8 are set around the block SB0, the offset is set at (0, 0) when the absolute value of a difference between the accumulation result of the block SB0 and the accumulation result of the coding target block CB is the minimum. Also, when the absolute value of a difference between the accumulation result of the block SB8 and the accumulation result of the coding target block CB is the minimum, the offset is set at (16, 16).

[0054] In this manner, according to this embodiment, the offset is set without using a motion vector detected in a previous frame. Therefore, the offset can be set in an initial frame in the motion vector detection. As a result, a motion vector can be detected more efficiently than in the first embodiment.

[0055] Alternatively, with defining a part of the coding target block as a unit area and with setting plural comparison areas each having the same size as the unit area, a predetermined pixel calculation can be executed on each of these areas. Also, the unit area can include an area in the vicinity of the coding target block. Moreover, the unit area can be in any shape.

[0056] As the predetermined pixel calculation, for example, an accumulating calculation of a difference between adjacent pixels, a variance calculation of pixels or a frequency distribution calculation of pixels can be adopted in stead of the accumulating calculation. In addition, a calculation result can be provided with a weighting in accordance with the position of the area.

[0057] It is noted that the present invention is easily applicable to motion vector detection in accordance with rotation or enlargement/reduction of an image.

Claims

1. A motion vector detecting device comprising:

motion vector detecting means for calculating a correlation evaluation value indicating correlation between a coding target block in a target frame and a candidate block within a search range of a search frame and for detecting a motion vector with regard to said coding target block on the basis of said correlation evaluation values;

original search range storing means for storing pixel data of an original search range and for supplying pixel data read in accordance with a given memory address to said motion vector detecting means;

control means for generating a first search width that is a pixel width in the horizontal direction of said search range, a second search width that is a pixel width in the vertical direction of said search range and an offset value indicating a relative position of said search range to said original search range; and

address generating means for generating said memory address supplied to said original search range storing means on the basis of said first and second search widths and said offset value generated by said control means.

2. The motion vector detecting device of Claim 1,

wherein said motion vector detecting means supplies, to said control means, a detected motion vector and a correlation evaluation value corresponding to said motion vector, and

said control means generates said first and second search widths and said offset value on the basis of said motion vector and said correlation evaluation value supplied by said motion vector detecting means.

3. The motion vector detecting device of Claim 2,

wherein said control means defines a block in the same position as or in the vicinity of said coding target block in a frame previous to said target frame as a range setting reference block, and sets a size of said search range on the basis of a correlation evaluation value corresponding to said range setting reference block.

4. The motion vector detecting device of Claim 2,

wherein said control means defines a block in the same position as or in the vicinity of said coding target block in a frame previous to said target frame as a range setting reference block, and sets a position of said search range on the basis of a motion vector with regard to said range setting reference block.

5. The motion vector detecting device of Claim 2,

wherein said control means defines a block in the same position as or in the vicinity of said coding target block in a frame previous to said target frame as a range setting reference block, and sets said original search range as said search range when correlation indicated by a correlation evaluation value corresponding to said range setting reference block is comparatively small, while sets a size of said search range to be smaller than a size

of said original search range and sets a position of said search range on the basis of a motion vector with regard to said range setting reference block when correlation indicated by the correlation evaluation value of said range setting reference block is comparatively large.

- 5 6. The motion vector detecting device of Claim 1,
wherein said offset value corresponds to components of a vector starting at a search start point of said original search range and ending at a search start point of said search range.
7. The motion vector detecting device of Claim 1,
10 wherein said address generating means includes:

a first counter for counting a clock;
a comparator for comparing said first search width with a count value of said first counter and, when said first search width accords with the count value, resetting the count value of said first counter and outputting a count-up signal;
15 a second counter for counting said count-up signal; and
a memory address convertor for generating said memory address supplied to said original search range storing means in accordance with a predetermined formula on the basis of said offset value and the count values of said first and second counters.
20
8. The motion vector detecting device of Claim 1,
wherein said control means generates, in addition to said first and second search widths and said offset value, a first subsample number corresponding to a subsampling rate in the horizontal direction of said search range and a second subsample number corresponding to a subsampling rate in the vertical direction of said search range, and
25

said address generating means generates said memory address supplied to said original search range storing means on the basis of said first and second search widths, said first and second subsample numbers and said offset value generated by said control means.
30
9. A method of detecting a motion vector with regard to a coding target block, comprising:

a first step of determining whether or not a predetermined area in a target frame includes a portion similar to said coding target block;
35 a second step of, when it is determined that a similar portion is included in said first step, setting a first area in said search frame including said similar portion and smaller than said predetermined area as a search range for motion vector detection; and
a third step of, when it is determined that a similar portion is not included in said first step, setting an area larger than said first area as said search range for motion vector detection.
40
10. The method of detecting a motion vector of Claim 9,
wherein, determination of said first step is made on the basis of a correlation evaluation value corresponding to a motion vector detected with regard to a range setting reference block that is a block in the same position as or in the vicinity of said coding target block in a frame previous to a target frame, and
45

in said second step, a position of said search range is set on the basis of said motion vector detected with regard to said range setting reference block.
11. The method of detecting a motion vector of Claim 9,
50 wherein said first step includes a step of executing a predetermined pixel calculation on each of a unit area including at least a part of said coding target block and plural comparison areas each having the same size as said unit area and provided in the vicinity of the same position as said coding target block in said search frame, and determination of said first step is made on the basis of results of said pixel calculation, and

55 in said second step, a comparison area most similar to said unit area is selected from said plural comparison areas on the basis of the results of said pixel calculation, and a position of said search range is set on the basis of a position of said selected comparison area.

12. A method of detecting a motion vector comprising:

a search range setting step of setting a search range for motion vector detection; and
a motion vector detecting step of detecting a motion vector with regard to a coding target block on the basis of
5 correlation with a candidate block in said search range,
wherein said search range setting step includes a step of setting a size of said search range on the basis of a
correlation evaluation value corresponding to a motion vector detected with regard to a range setting reference
block that is a block in the same position as or in the vicinity of said coding target block in a frame previous to
a target frame.

13. A method of detecting a motion vector comprising:

a search range setting step of setting a search range for motion vector detection; and
a motion vector detecting step of detecting a motion vector with regard to a coding target block on the basis of
15 correlation with a candidate block in said search range,
wherein said search range setting step includes a step of setting a position of said search range on the basis
of a motion vector detected with regard to a range setting reference block that is a block in the same position
as or in the vicinity of said coding target block in a frame previous to a target frame.

Fig. 1

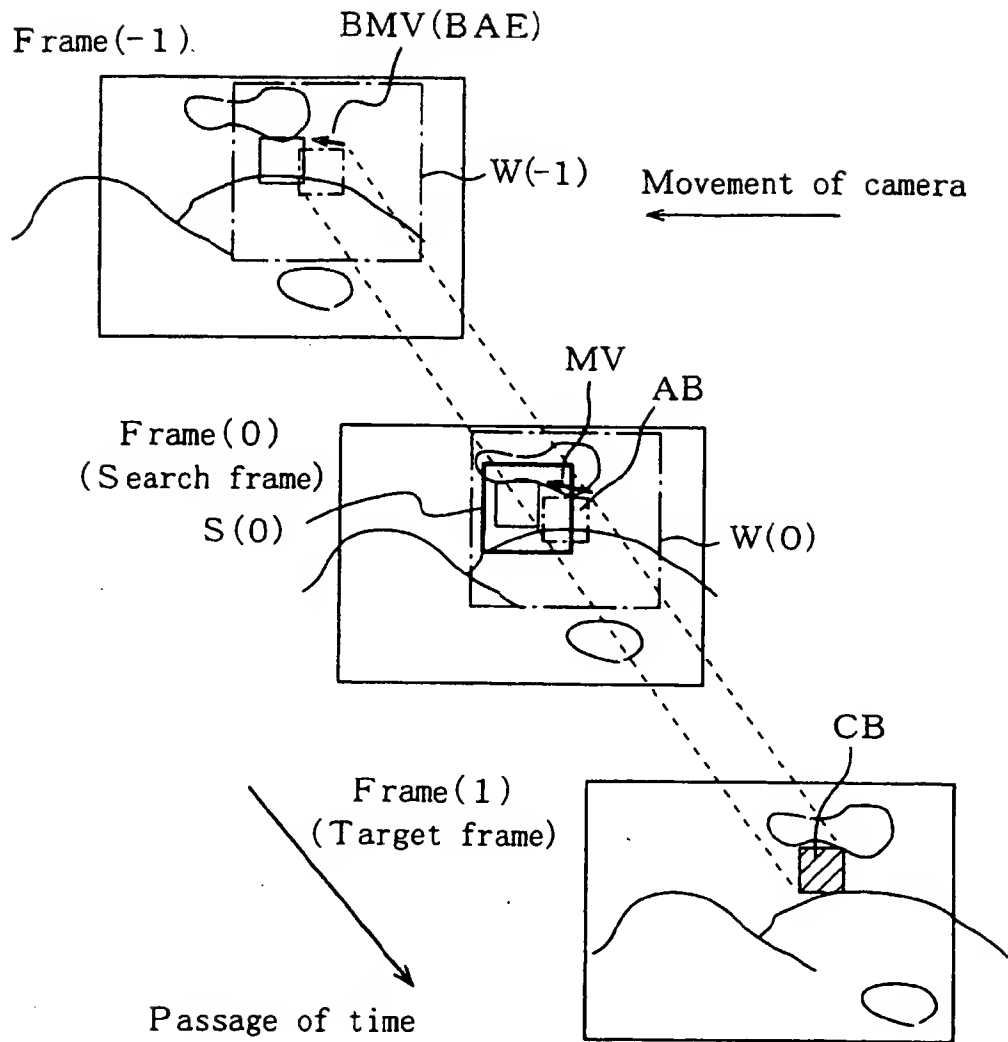


Fig. 2 (a)

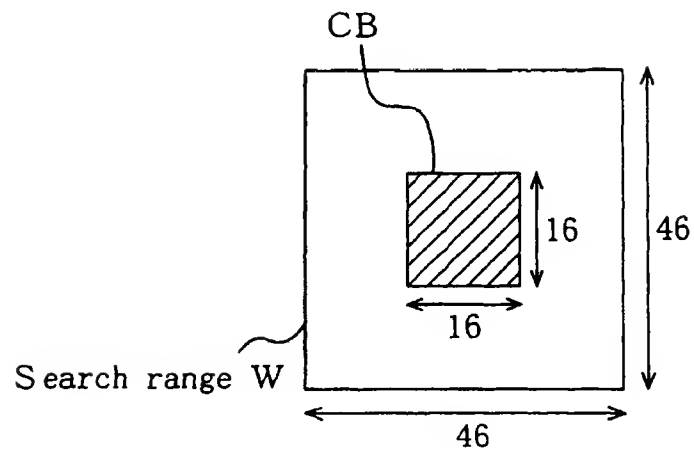
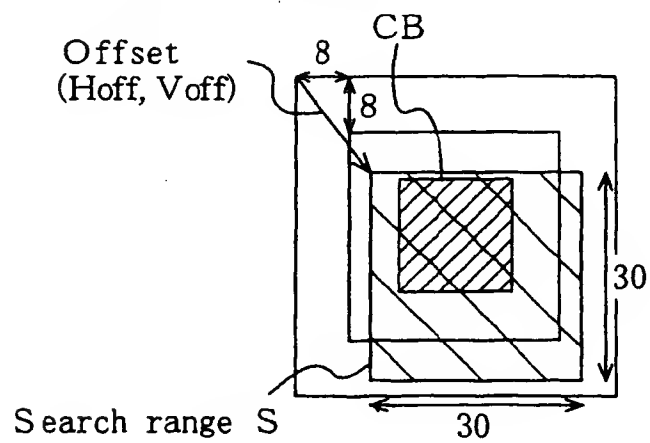


Fig. 2 (b)



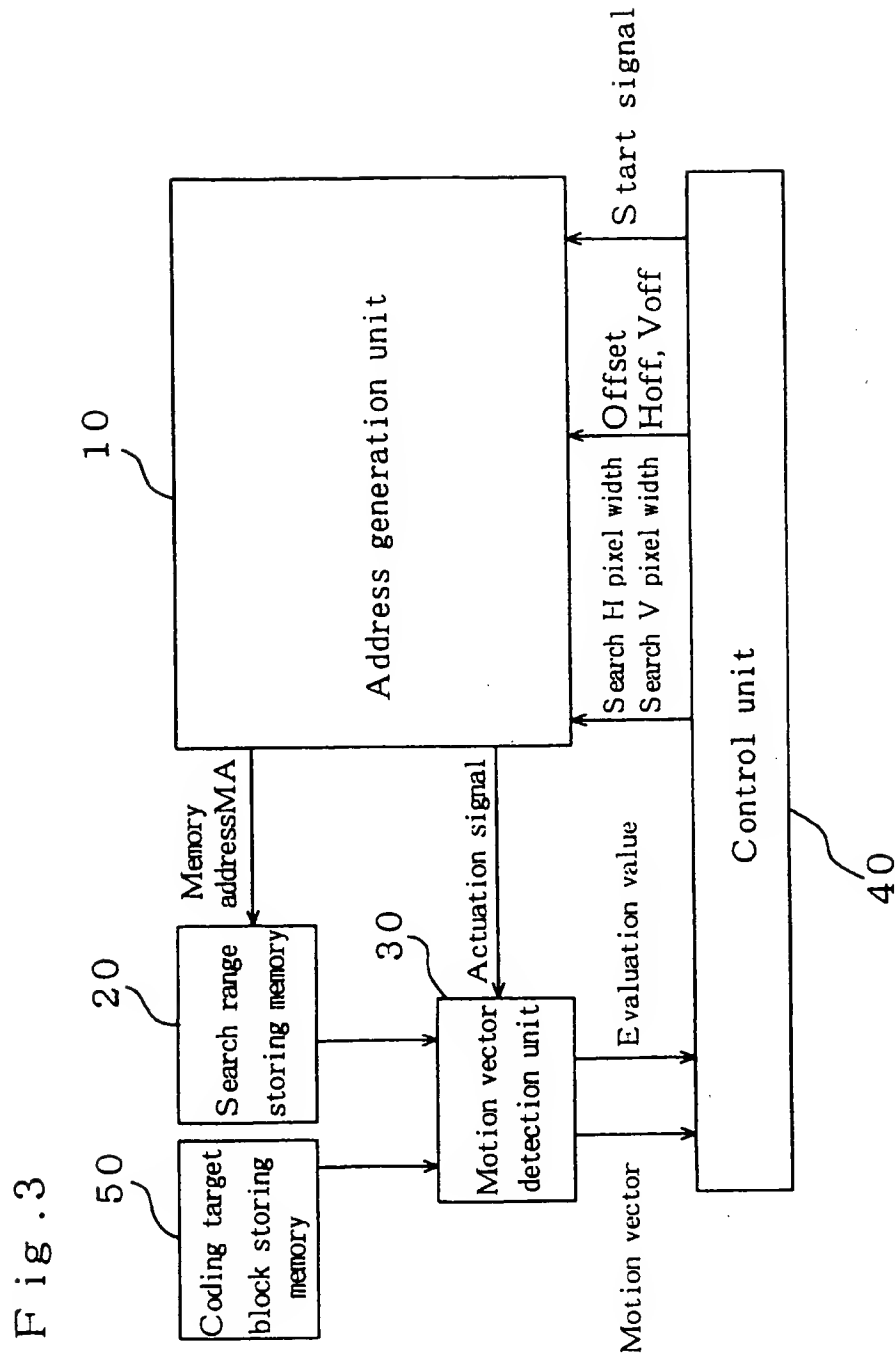


Fig. 4

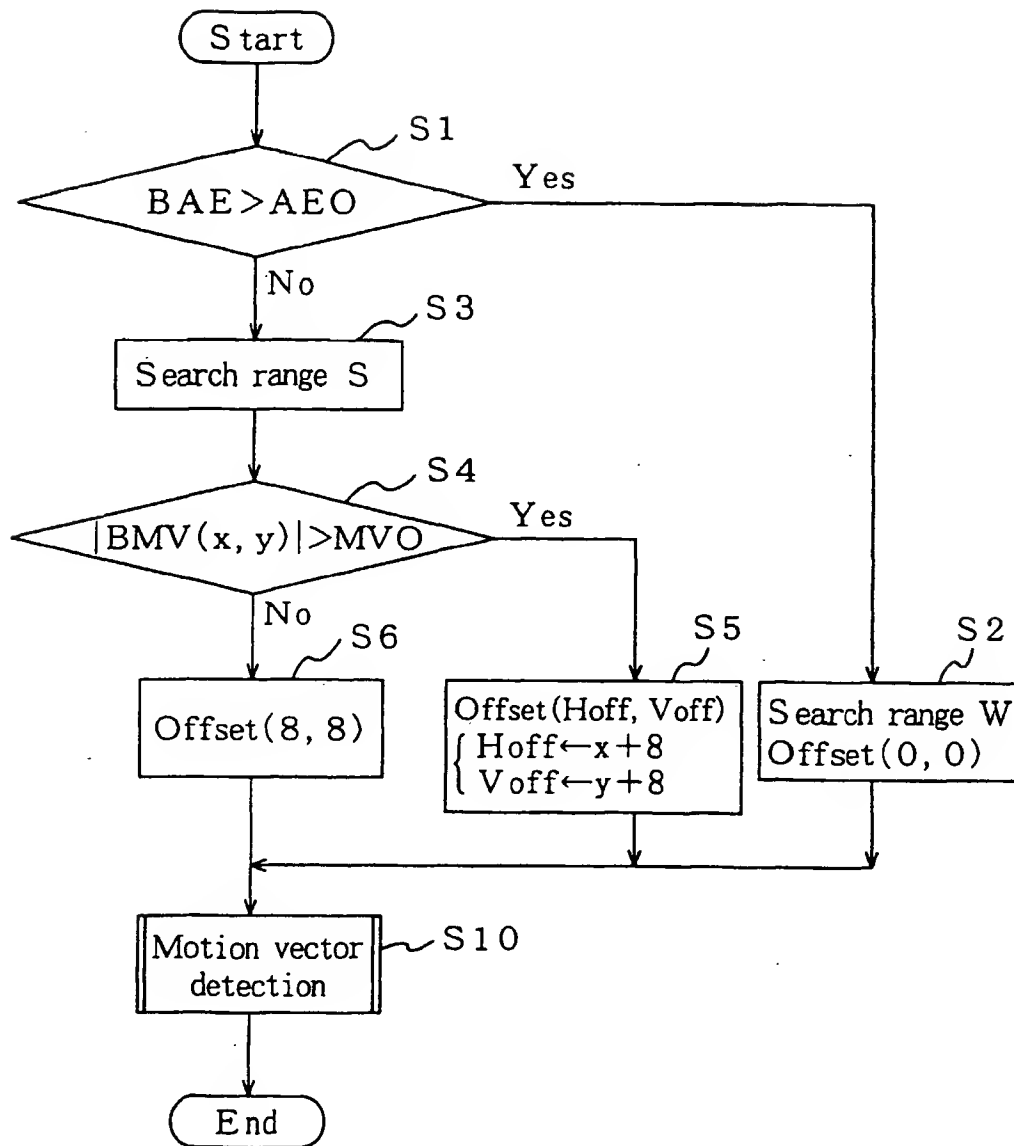


Fig. 5

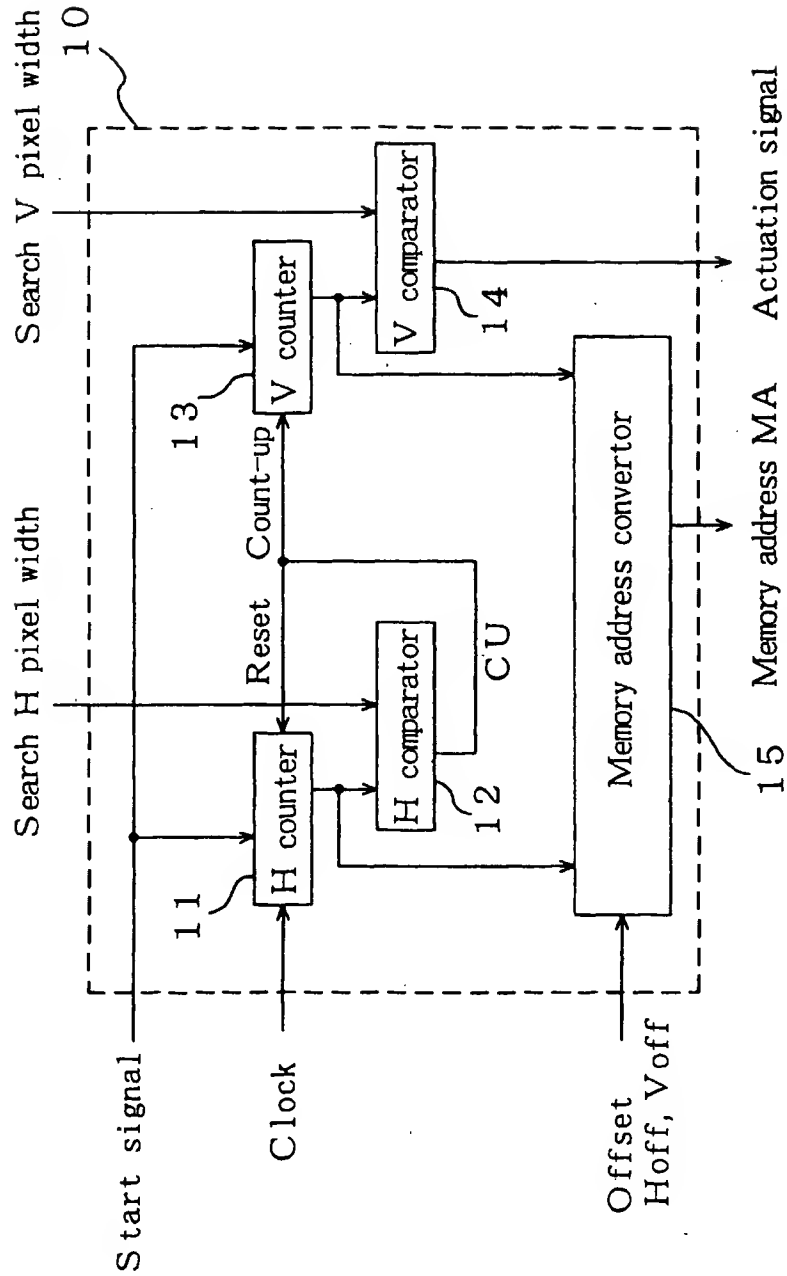


Fig. 6

Offset (2, 1)

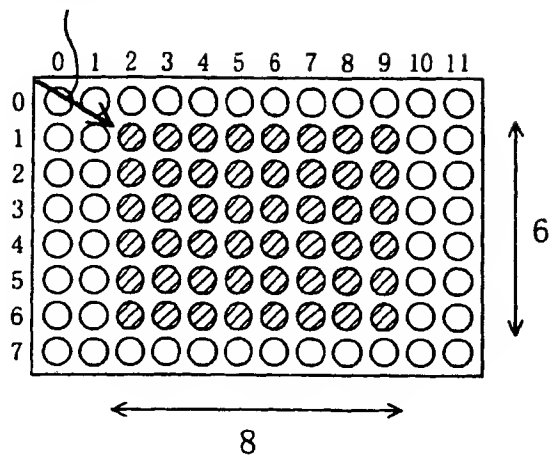


Fig. 7

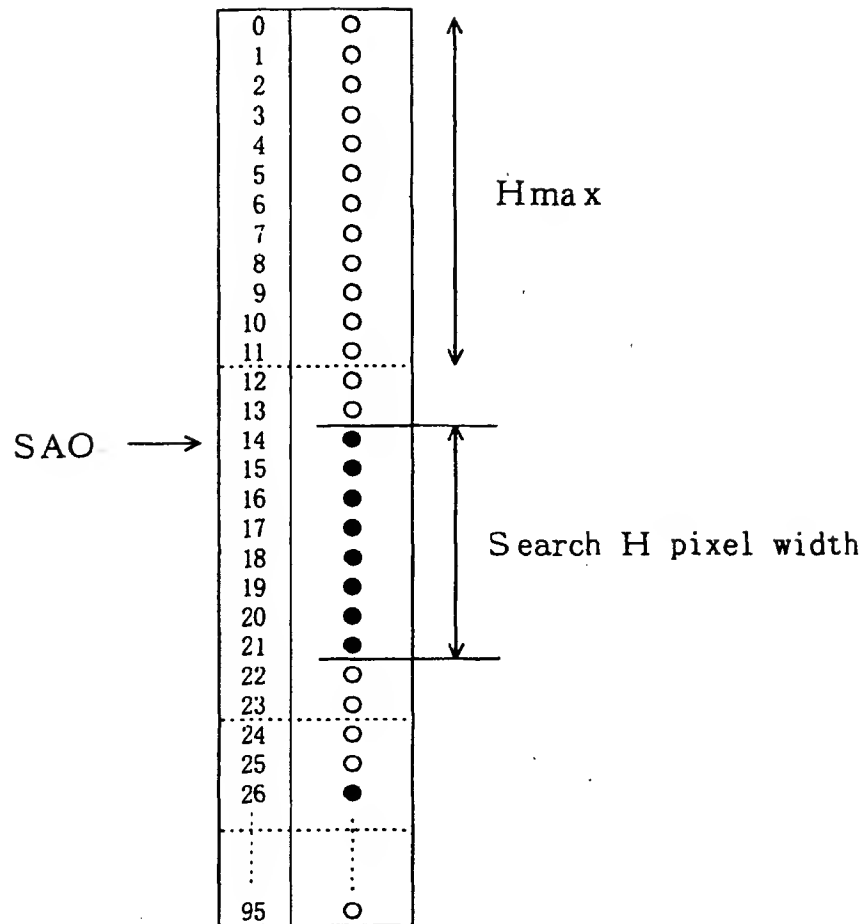


Fig. 8

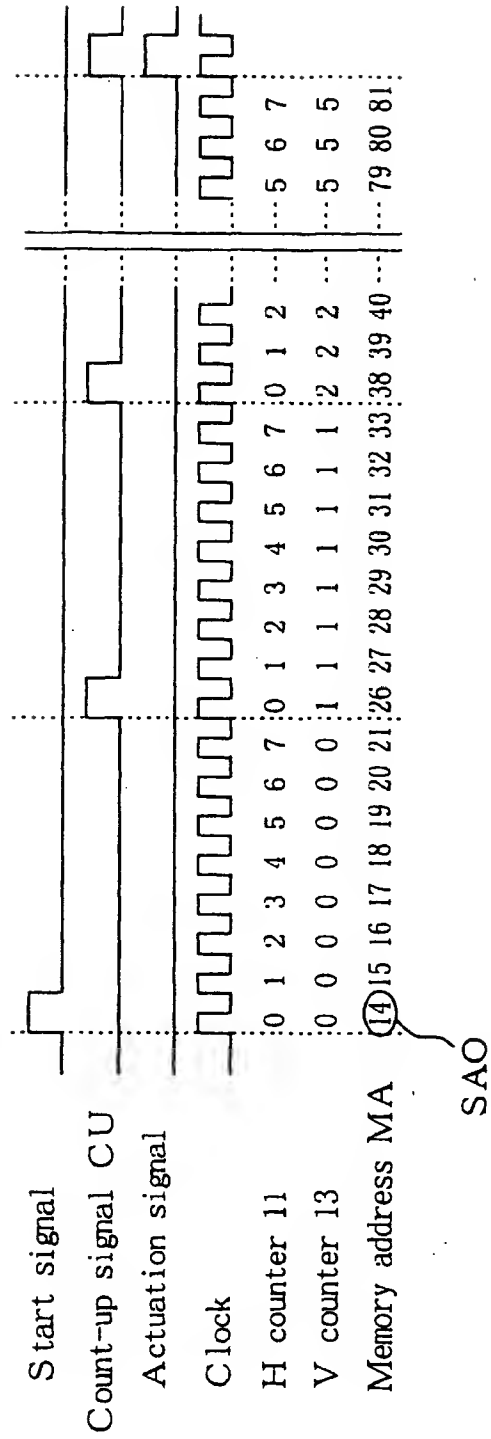


Fig. 9

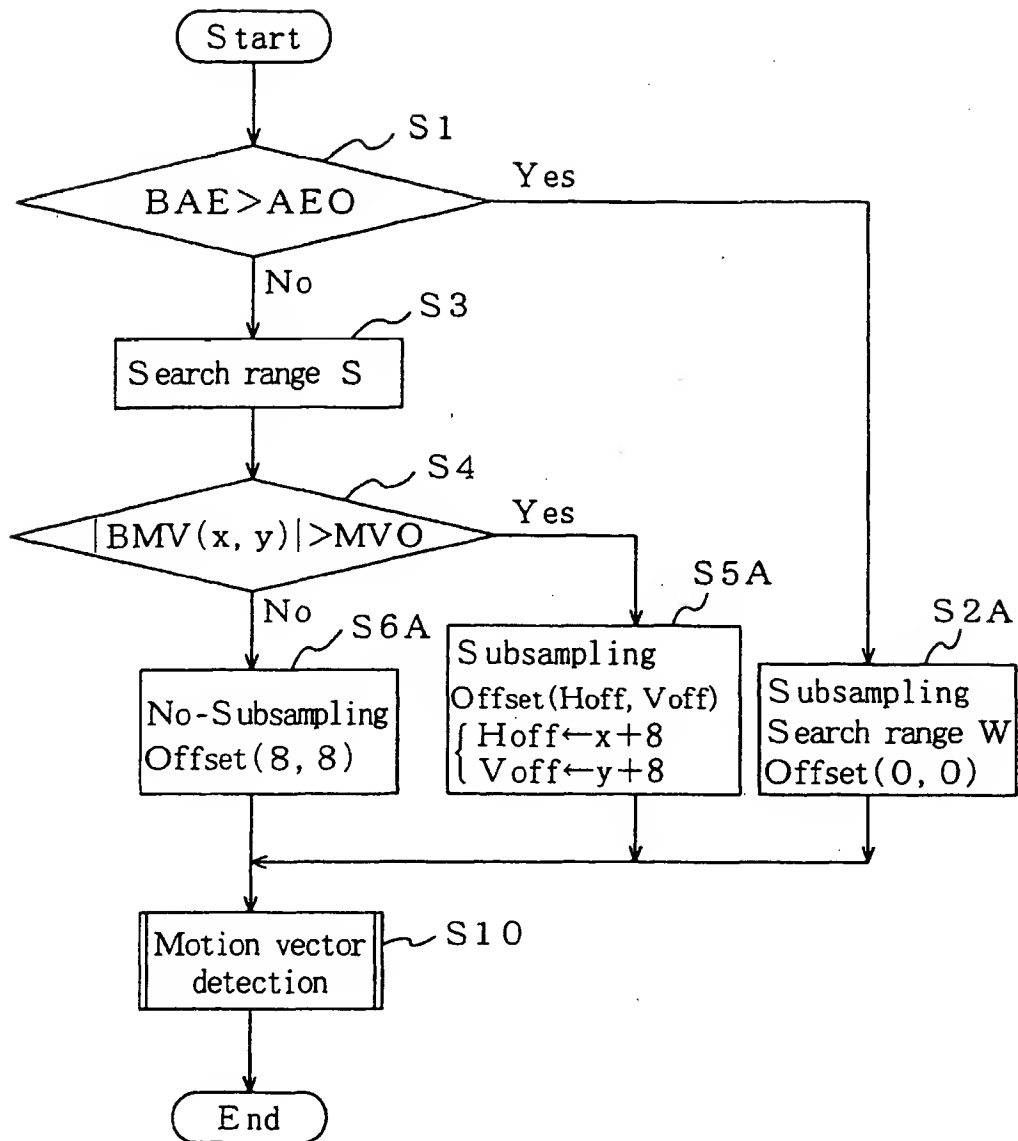


Fig.10(a)

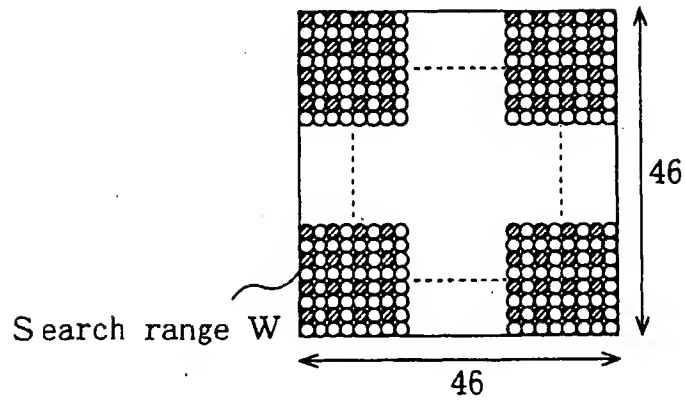


Fig.10(b)

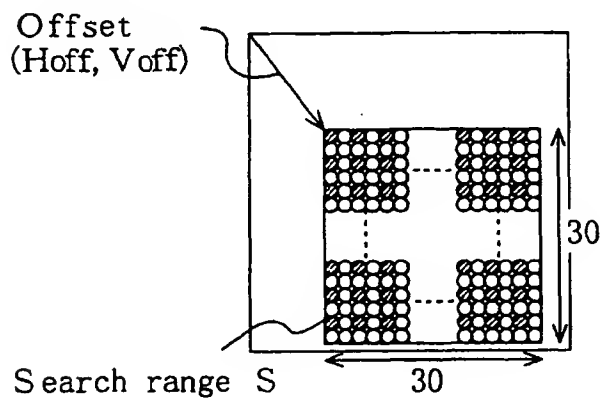


Fig.10(c)

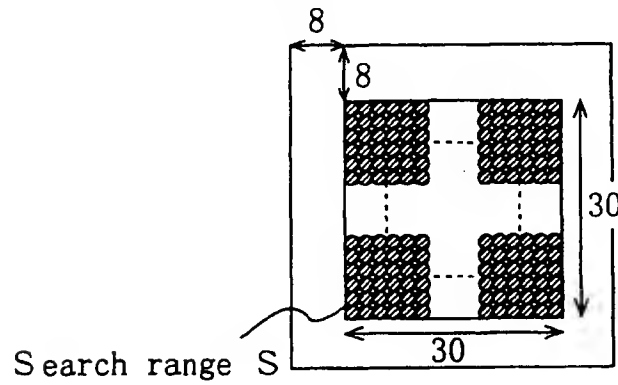


Fig. 11

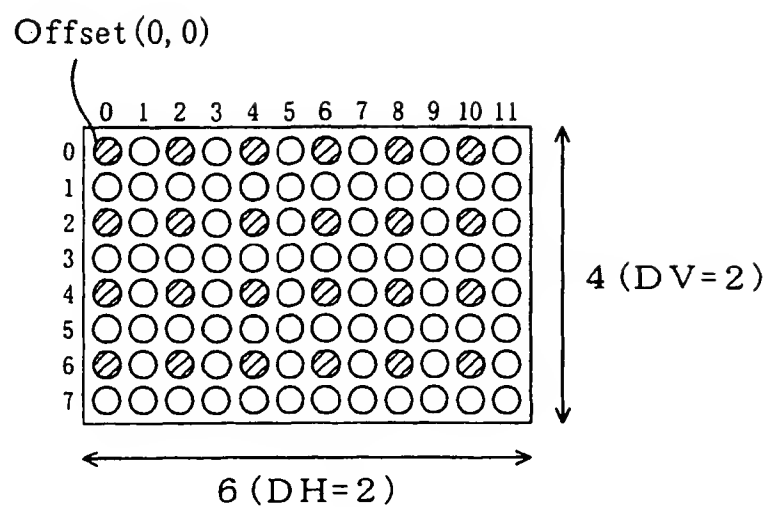


Fig. 12

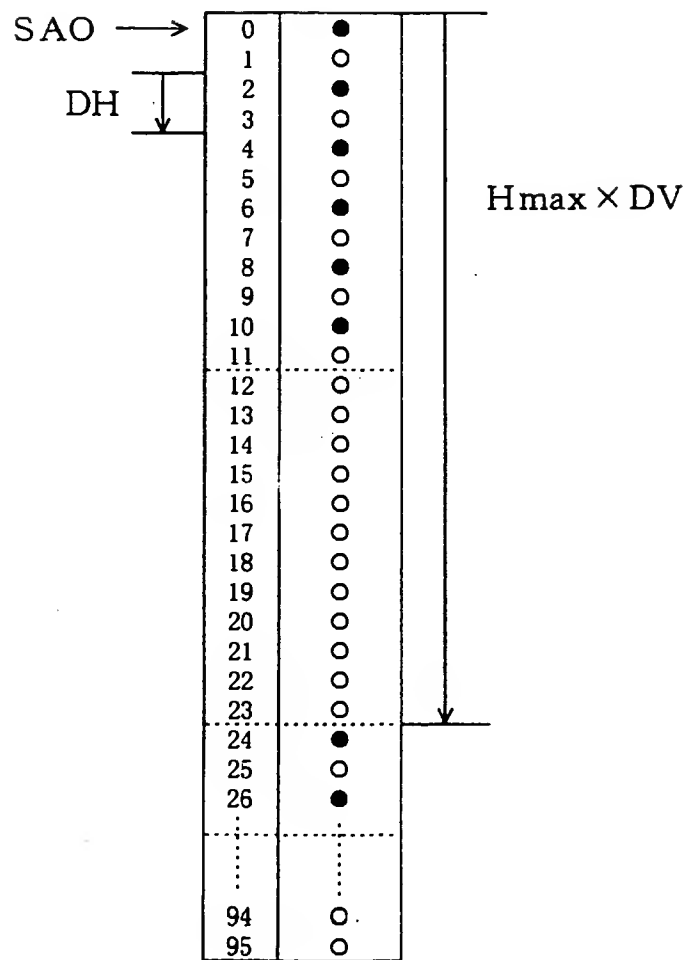


Fig. 13

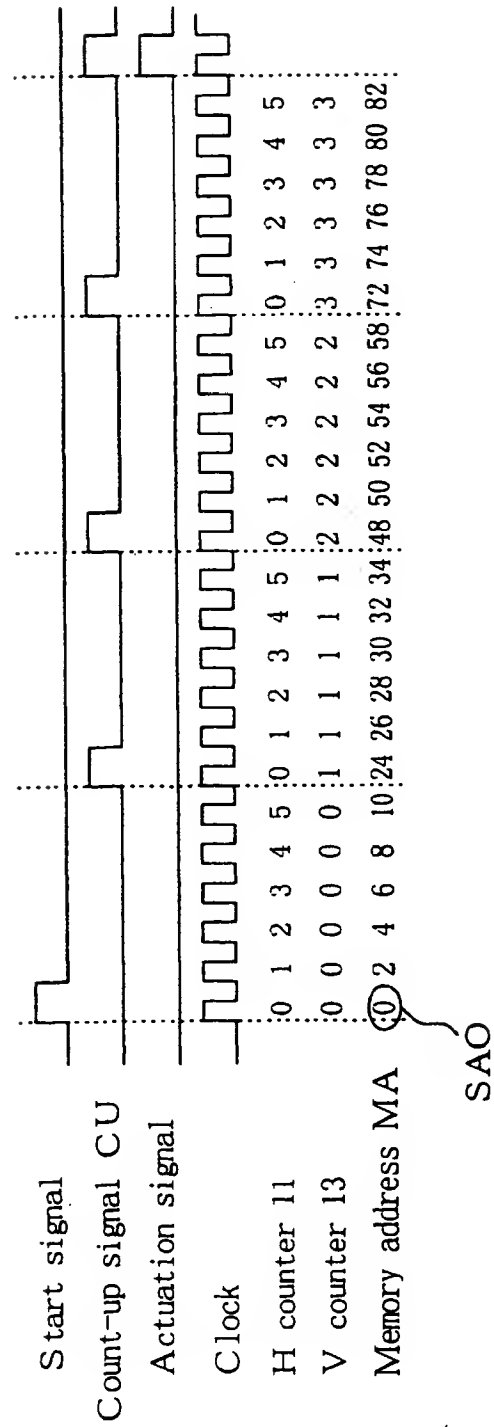


Fig. 14

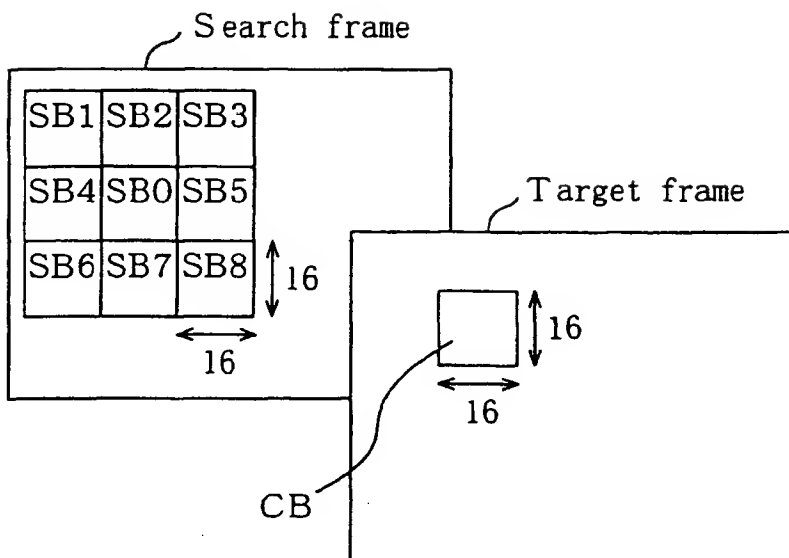


Fig. 15

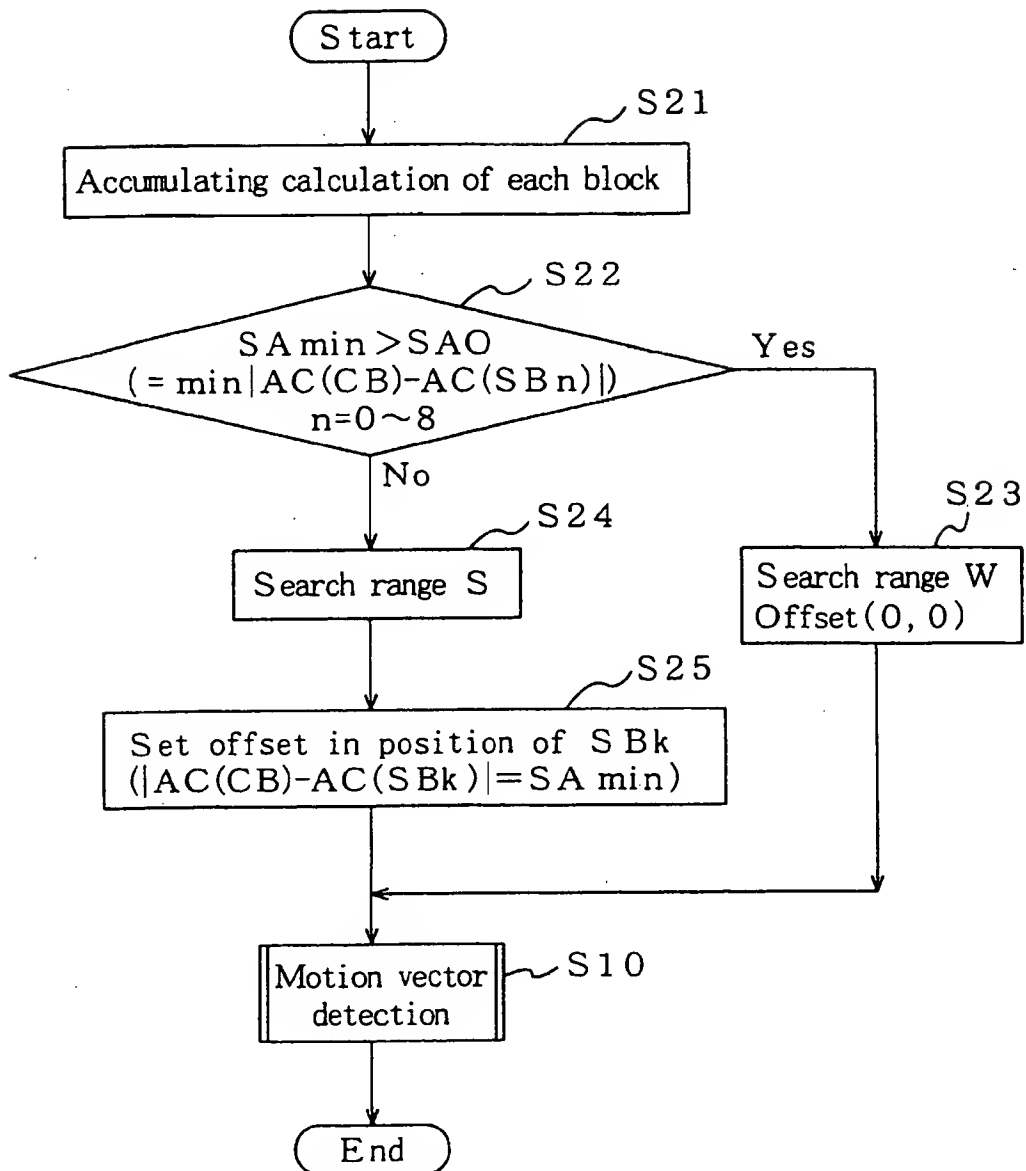
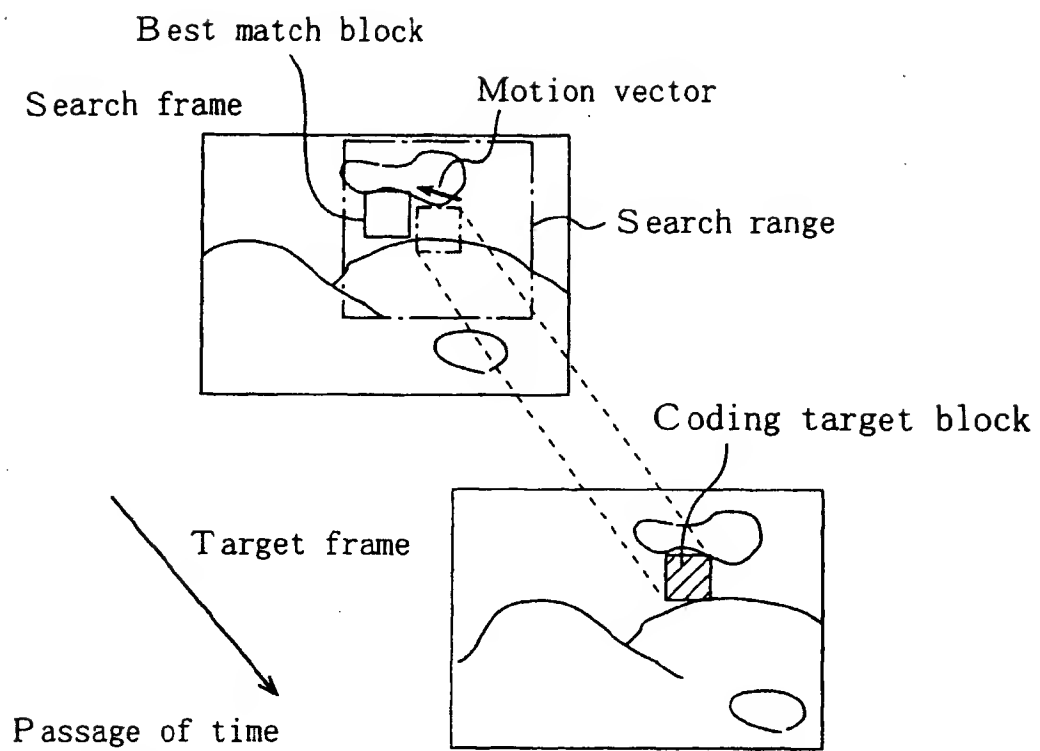


Fig. 16



(19)



Europäisches Patentamt

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(11)

EP 0 896 300 A3

(12)

EUROPEAN PATENT APPLICATION

(88) Date of publication A3:
08.09.1999 Bulletin 1999/36

(51) Int. Cl.⁶: G06T 7/20, H04N 5/14,
H04N 7/36

(43) Date of publication A2:
10.02.1999 Bulletin 1999/06

(21) Application number: 98114578.2

(22) Date of filing: 03.08.1998

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
Designated Extension States:
AL LT LV MK RO SI

(30) Priority: 07.08.1997 JP 21282697

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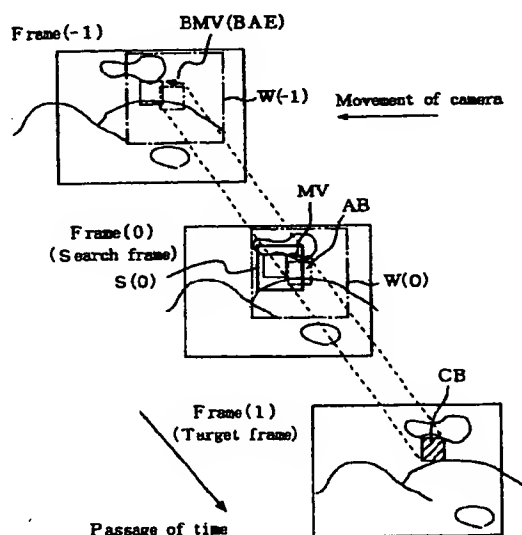
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(54) Device and method for motion vector detection

(57) According to the invention, a motion vector can be accurately and efficiently detected with regard to any type of images having a variety of characteristics. A motion vector detection unit detects a motion vector with regard to a coding target block on the basis of correlation with a candidate block within a search range. A search range storing memory stores pixel data of an original search range, and supplies, to the motion vector detection unit, pixel data of a search range in accordance with a memory address. A control unit sets the size and the position of the search range on the basis of the motion vector and the correlation evaluation value supplied from the motion vector detection unit, and generates pixel widths in the horizontal and vertical directions of the search range and an offset value corresponding to the relative position to the original search range. An address generation unit generates the memory address to be supplied to the search range storing memory on the basis of the pixel widths and the offset value of the search range generated by the control unit.

Fig. 1



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Office

EUROPEAN SEARCH REPORT

Application Number
EP 98 11 4578

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.5)
X	EP 0 652 678 A (AT & T CORP) 10 May 1995 * column 3, line 17-54; figure 3B *	1,13	G06T7/20 H04N5/14 H04N7/36
X	EP 0 689 359 A (SAMSUNG ELECTRONICS CO LTD) 27 December 1995 * abstract; figures 3,4A,4B *	1,13	
A	HARTWIG S ET AL: "DIGITALE BILDCODIERUNG. TEIL 6. BEWEGUNGSKOMPENSIERTE INTERFRAME -DPCM" FERNSEH UND KINOTECHNIK, vol. 46, no. 6, 1 January 1992, pages 416-421, 424, XP000304847	1-8,13	
X	HILDENBRAND K ET AL: "MOTION ESTIMATION IN THE HERMES VIDEOCODEC" FREQUENZ, vol. 47, no. 11/12, 1 November 1993, pages 303-309, XP000426383 * paragraph 3.6; figure 5 *	12	
A		9	TECHNICAL FIELDS SEARCHED (Int.Cl.6)
D,X	PATENT ABSTRACTS OF JAPAN vol. 096, no. 006, 28 June 1996 & JP 08 032969 A (CANON INC), 2 February 1996 * abstract *	12	G06T H04N
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A	GB 2 265 516 A (SONY UK LTD) 29 September 1993 * abstract; figures 5,11,12 *	9,12	
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The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
BERLIN		5 July 1999	Jonsson, P.O.
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			

EPO FORM 1503 03 82 (P04C01)



European Patent
Office

Application Number
EP 98 11 4578

CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing more than ten claims.

- ☐ Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid, namely claim(s):
- ☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

see sheet B

- ☒ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
- ☐ As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.
- ☐ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:
- ☐ None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 98 11 4578

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	LIANG-WEI LEE ET AL: "DYNAMIC SEARCH-WINDOW ADJUSTMENT AND INTERLACED SEARCH FOR BLOCK- MATCHING ALGORITHM" IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS FOR VIDEO TECHNOLOGY, vol. 3, no. 1, February 1993, pages 85-87, XP000334581 * paragraph I - paragraph II * -----	9,12	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
The present search report has been drawn up for all claims			
Place of search BERLIN		Date of completion of the search 5 July 1999	Examiner Jonsson, P.O.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

EPO FORM 1503 03 82 (P4/C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 98 11 4578

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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05-07-1999

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European Patent
Office

**LACK OF UNITY OF INVENTION
SHEET B**

Application Number

EP 98 11 4578

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. Claims: 1-8, 13

A motion vector detecting device and method wherein means/steps are provided for determining the position of a search range on the basis of an original search range and an offset value based on global motion;

2. Claims: 9-11, 12

A motion vector detecting method wherein steps are provided for determining a smaller size of the search range if a predetermined area in a target frame includes a portion similar to a coding block, and for determining a larger size of the search range if a predetermined area in a target frame does not include a portion similar to a coding block.